


The specifications for the **LTC<sup>®</sup>1337** have been changed. The new specifications are shown below in **bold**. For complete specifications, typical performance characteristics and applications information, please see the **LTC1337** data sheet.

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## DC ELECTRICAL CHARACTERISTICS

PARAMETER	CONDITIONS		MIN	TYP	MAX	UNITS
<b>Any Driver</b>						
Output Voltage Swing	$R_L = 3k$ to GND Positive	●	5.0	7.0		V
	$R_L = 3k$ to GND Negative	●	-5.0	-6.5		V
Logic Input Voltage Level	Input Low Level ( $V_{OUT} = \text{High}$ )	●		1.4	0.8	V
	Input High Level ( $V_{OUT} = \text{Low}$ )	●	2.0	1.4		V
Logic Input Current	$V_{IN} = 5V$	●			5	$\mu A$
	$V_{IN} = 0V$	●			-5	$\mu A$
Output Short-Circuit Current	$V_{OUT} = 0V$			$\pm 10$		mA
Output Leakage Current	SHUTDOWN, $V_{OUT} = \pm 20V$ (Note 3)	●		10	<b>500</b>	$\mu A$
<b>Any Receiver</b>						
Input Voltage Thresholds	Input Low Threshold	●	0.8	1.3		V
	Input High Threshold	●		1.7	2.4	V
Hysteresis		●	0.1	0.4	1	V
Input Resistance	<b><math>-10V \leq V_{IN} \leq 10V</math></b>		3	5	7	k $\Omega$
Output Voltage	Output Low, $I_{OUT} = -1.6mA$ ( $V_{CC} = 5V$ )	●		0.2	0.4	V
	Output High, $I_{OUT} = 160\mu A$ ( $V_{CC} = 5V$ )	●	3.5	4.8		V
Output Short-Circuit Current	Sourcing Current, $V_{OUT} = 0$		15	20		mA
	Sinking Current, $V_{OUT} = V_{CC}$		-15	-40		mA
Output Leakage Current	SHUTDOWN, $0 \leq V_{OUT} \leq V_{CC}$ (Note 3)	●		1	10	$\mu A$

## PIN FUNCTIONS

**ON/OFF:** TTL/CMOS Compatible Shutdown Pin. A logic low puts the device in the SHUTDOWN mode which reduces input supply current to less than 1 $\mu A$  and places all drivers and receivers in high impedance state.

**This pin cannot float.**

For further information regarding this specification notice contact:

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